



Attorney Docket SEL 173

#20/2811
11/22/02
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

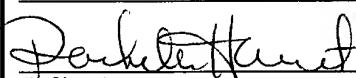
In Re Application of)
Ohtani et al.)
Serial No.: 09/535,836)
Filed: March 28, 2000)
For: Semiconductor Device Having)
Multi-layer Wiring)
Art Unit: 2811)
Examiner: H. Vu)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Commissioner for Patents
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on November 12, 2002

Rachelle Hammerquist

 11-12-02
Signature Date

Commissioner for Patents
Washington, D.C. 20231

AMENDMENT E

Sir:

In response to the Office Action dated August 14, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend the claims as follows:

Subj

28. (Third Amendment) A semiconductor device comprising:
a thin film transistor formed over a substrate, said thin film transistor having a semiconductor layer and a gate electrode adjacent to said semiconductor layer with a gate insulating film interposed therebetween;
a first insulating film comprising an organic material formed over said thin film transistor;